



PATENT APPLICATION

2184
26/09/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application

Applicant: Kondo et al.

Serial No. 09/723,768

Filed: November 28, 2000

For: APPARATUS AND METHOD FOR
PREVENTING AN ERRONEOUS
OPERATION AT THE TIME OF
DETECTION OF A SYSTEM FAILURE

Art Unit: 2184

Examiner: Puente, Emerson C.

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

23 Sept 03

Date
F-CLASS.WCM
Appl. February 20, 1998

Registration No. 29,367
Attorney for Applicant

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Technology Center 2100

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement mailed August 25, 2003, applicants elects Group II, claims 13-30, 32-33 and 35-36, without traverse. Accordingly, please cancel non-elected claims 1-12, 31 and 34, without prejudice.

Respectfully submitted,

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By

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September 23, 2003
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